



DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE Affiliated to JNTU Kakinada)

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FACULTY PROFILE

Name of the Faculty Lakshmi Sowjanya Koneru

Designation Assistant Professor

Date of Joining 7.6.2010

Nature of Association Regular

Email & Phone No Sowjanya5710@gmail.com & 8885516969

Department Electronics & Communication Engineering

Educational Background

1. M. Tech (VLSI System Design) from VNRVJIET, Hyderabad.
2. B. Tech (ECE) from NIET, Hyderabad.
3. Diploma in ECE from K.E.S Polytechnic for Women, Vijayawada.

Area of Specialization VLSI System Design

Research Areas NIL

Experience 11 Years



Sl. No	Institute	Designation	Period
2	Dhanekula Institute of Engineering & Technology	Asst.Prof	2010 - till date
1	K. L. College of Engineering	Lecturer	2006 - 2007

List of Publications (National and International Journals)

- 1) Low Power and Area Efficient Fused Floating Point FFT Architecture, International journal of scientific engineering and technology, ISSN 2319-8885 Vol.04, Issue.25, July-2015, Pages:4738-4740
- 2) Implementation of RISC processor using Radix-4 and Radix-8 Booth Encoded Multi-Modulus Multipliers, IJMETMR, ISSN/ISBN. 2348-4845.

K.L. Sowjanya