



DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY
(Approved by AICTE Affiliated to JNTU Kakinada)

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FACULTY PROFILE

Name of the Faculty	Naga Prasanthi Yerneni
Designation	Assistant Professor
Email & Phone No	nagaprasanthi@diet.ac.in & 9441786778
Department	Electronics and communications Engineering
Educational Background	Registered PhD Program in KL Deemed University on Jan 2021 M.Tech From JNTU, Kakinada affiliated college with 79.64% B. Tech From JNTU, Kakinada affiliated college with 70.14% Intermediate with 89% SSC with 86.6%
Specialization	Embedded Systems
Experience	8 Years
Industrial Experience	Nil



Sl. No	Institute	Designation	Period
1	DhaneKula Institute of Engineering and Technology	Asst.Prof	2013 - till date
2	QIS Institute of technology, Ongole	Asst.Prof	2012 - 2013

Area of Research: Signal Processing Via Deep Learning

Subjects Taught:

- Switching Theory and Logic Design
- Pulse and Digital Circuits
- Linear IC Application
- Linear and Digital IC applications
- Microprocessors and Micro controllers
- Professional Ethics and Human Recourses
- Computer Organization and Architecture
- Electronic Switching systems
- Embedded systems.

Laboratories Handled:

- Linear IC Applications lab
- Pulse and digital circuits lab
- Microprocessors and Micro controller lab
- Digital Signal Processing Lab
- Digital Communications Lab

List of Publications

- Journals: An efficient tree structure 32-Bit Brent -Kung Adder for Reducing time delay and memory utilization " Journal of semiconductor devices and circuits Volume 5, issue 3. 2019-20
- Journals: "Implementation of 1*5 router architecture for speed critical applications" journal of advanced in dynamical and control systems, Vol .9, No. 3, 2017

National/International Conferences

- Attended INTERNATIONAL CONFERENCE on Allied Electrical and Communication Systems 8-10 December 2016.

Faculty Development Programs:

- Attended STTP on **APPLIED MACHINE LEARNING ALGORITHMS AND APPLICATIONS** (PHASE-III) from 14th to 19th December 2020 organized by Annamacharya institute of Technology and Sciences: Tirupati
- Attended FDP on **APPLIED MACHINE LEARNING AND DEEP LEARNING** from 15th June to 20th June 2020 Organized by APSSDC
- Conducted a Faculty development program on ASLK kits which are sponsored by Texas Instruments in 2017 at our ECE Department, Dhanekula institute of Engineering and technology.
- Attended FDP for three days on **Custom IC design and ASIC Implementation** using cadence EDA Tools from on 3rd ,4th and 5th August 2016.
- Attended FDP for two days on **Analog system design using ASLK Pro** from 16th and 17th Feb. 2016.

Attended Workshops:

- Attended Embedded workshop conducted in Dhanekula Institute of Engineering and Technology for the academic year 2014-15
- Attended Advanced Trends in Embedded Systems, ATES – 2014 ON 28TH Feb – 1st March 2014, Conducted by PVPSIT, Kanuru, Vijayawada.

Achievements / Awards:

- Received certificate of Appreciation as **BEST MENTOR** for the academic year 2017-18 from Department of ECE.

Certification courses:

- AI For Everyone
- Introduction to Electronics
- Introduction to the Internet of Things and Embedded Systems

Projects handled:

- Gesture Control Wireless Car Using Flex Sensors for the academic year 2019-20
- Women protection system using smart security devices for the academic year 2019-20
- RFID Based School Bus Tracking and Security SYSTEM for the academic year 2017-18
- Unmanned quadcopter for the academic year 2014-15

**Place:** Ganguru

Naga Prasanthi Yerneni

Date: 30.05.2021