



# DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE Affiliated to JNTU Kakinada)

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## FACULTY PRPOFILE

Name of the Faculty	Ramakrishna Yarlagadda
Designation	Assistant Professor
Date of Joining	1.12.2016
Nature of Association	Regular
Email & Phone No	<a href="mailto:Yarlagadaramakrishna119@gmail.com">Yarlagadaramakrishna119@gmail.com</a> & 9052988244
Department	Electronics and Communication Engineering
Educational Background	1. M.Tech From JNTUK, Kakinada 2. B.Tech From JNTUK, Kakinada
Area of Specialization	VLSI Design
Research Areas	Low Power VLSI
Experience	4 Year 5 Months



Sl. No	Institute	Designation	Period
1	DhaneKula Institute of Engineering & Technology	Asst. Prof	Dec 2016 - till date

### List of Publications (National and International Journals)

1. Published a paper titled “**Low Power Efficient MAC Unit using Proposed Carry Select Adder**” proceedings of International Journal & Magazine of Engineering, Technology, Management and Research (IJMETMR – Oct, 2015), Vol. No. 02, Issue No. 10, Page no. 1185 – 1191, ISSN: 2348-4845.

Ramakrishna Yarlagadda