



## DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE Affiliated to JNTU Kakinada)

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### FACULTY PRPOFILE

<b>Name of the Faculty</b>	K. Siva Nagendra	
<b>Designation</b>	Assistant Professor	
<b>Date of Joining</b>	14-02-2020	
<b>Nature of Association</b>	Regular	
<b>Email &amp; Phone No</b>	siva.evergreenec@gmail.com & 9014574245	
<b>Department</b>	Electronics and Communication Engineering	
<b>Educational Background</b>	1. M.Tech (VLSI & Embedded Systems) from GMRIT, Rajam 2. B.Tech (ECE) from STIET, Garividi	
<b>Areas of Specialization</b>	Digital VLSI Design	
<b>Research Areas</b>	Digital VLSI Design, Architecture Optimization, Approximate computing.	
<b>Experience</b>	10 Years	

Sl. No	Institute	Designation	Period
1	DhaneKula Institute of Engineering & Technology, Ganguru	Assistant Professor	14-02-2020 - till date
2	Aditya college of Engineering, Surampalem	Assistant Professor	09-06-2014 - 12-02-2020
3	St. Theresa Institute of Engineering & Technology, Garividi	Assistant Professor	01-12-2009 - 31-12-2011

#### List of Publications (National and International Journals):

1. Mr.Kunapareddy S Nagendra , Mr.D.Suresh "Improved Fused Floating Point Add-Subtract Unit", International Journal of Research in Computer and Communication Technology, Vol 2, Issue 9, pages 786-792, September -2013.
2. T.Veerababu , K.Siva Nagendra "VLSI Architecture Area Efficient FFT Or IFFT Processor Using Vedic Multiplexing", International Journal of Research in computer and communication Technology , Vol 6, Issue- 8, pages 269-271, August - 2017.

3. K. V. S. Kumar , K. Siva Nagendra “Assertion based Verification of AXI Protocol using UVM”, International Journal of Research in Engineering, Science and Management Volume-2, Issue-1, pages 560-563, January-2019.

**Achievements / Awards etc.,**

- |   |                          |           |
|---|--------------------------|-----------|
| 1. Microprocessor and Microcontroller             | (NPTEL Certification)    | APR-2018. |
| 2. Digital Circuits                               | (NPTEL Certification)    | OCT-2018. |
| 3. Analog Circuits                                | (NPTEL Certification)    | APR-2019. |
| 4. Basic Electric Circuits                        | (NPTEL Certification)    | DEC-2019. |
| 5. Hardware Description Languages for FPGA Design | (Coursera Certification) | MAY-2020. |
| 6. AI for Everyone                                | (Coursera Certification) | JUN-2020  |



**K.Siva Nagendra**